

# MODEL 6115G-3 GPS SYNCHRONIZED IRIG B TIME CODE GENERATOR

## FEATURES

- Twelve channel GPS receiver with active antenna.
- IRIG B time code generator.
- Front Panel Time Display.
- Five IRIG B serial outputs.
- Can be synchronized to GPS, IRIG B or external 1PPS clock reference.
- Parallel Status/Control Port and RS-232 Data Port.
- Powered by 28 Volts DC.



## DESCRIPTION

The Model 6115G-3 is a twelve channel GPS synchronized IRIG B time code generator designed to provide a precise IRIG B serial time code output as well as a 1PPS time pulse. The unit automatically acquires all in-view satellites upon power up and locks an internal IRIG B time code generator to the GPS time reference. If the GPS lock is lost the 6115G-3 will automatically switch to an internal clock and continue generating the output IRIG B signal. No discernible change in the IRIG B output will occur due to this transition.

In the event that a GPS signal is not available the 6115G-3 can be locked to an external IRIG B serial time code signal. The selection of GPS or IRIG as the synchronization source is via a front panel switch or it may be done remotely via a TTL signal or contact closure.

The 6115G-3 may also be synchronized to an external 1 PPS such as a time mark signal from another timing device. When applied, the 6115G-3 internal clock will be reset to the closest second. It is therefore necessary to have previously set the clock recently enough to be certain that the drift would be less than one half second.

The 6115G-3 may also be used as a stand-alone unit to generate an IRIG B signal. The time-of-year may be set by the user via front panel switches. An LED readout displays the days, hours, minutes and seconds. In the event of a power failure the 6115G-3 will no longer output a serial time code however the time will continue to advance by automatically switching to a battery backed-up internal clock. The IRIG output will resume, without resetting, upon reapplication of power.

The unit has five buffered IRIG B outputs as well as status and synchronized clock signal outputs.

