



TECHNICAL DESCRIPTION

MODEL 6115-9D TIME CODE TRANSLATOR

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**MODEL 6115-9D
TIME CODE TRANSLATOR**

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MODEL 6115-9D
TIME CODE TRANSLATOR

1.0 GENERAL

The Model 6115-9D provides for the conversion of an applied IRIG B120 serial time code to an IRIG A000 serial time code. Three buffered IRIG A outputs are provided.

Additionally there are three buffered, individually adjustable, IRIG B outputs, which provide distribution of the applied IRIG B serial time code.

An LCD readout displays the days, hours, minutes and seconds. An LED indicates IRIG signal lock status.

The unit is housed in a 8.5 by 8.5 by 2.5 inch aluminum enclosure. It is powered by 9 to 36 Volts AC/DC power.

2.0 CHARACTERISTICS

2.1 TIME CODE SYNCHRONIZATION

The 6115-9D internal clock is automatically synchronized to the applied time code upon receipt of a valid IRIG B signal. If the unit loses the lock after initial synchronization, it will continue to operate on the internal clock. No discernible change in the IRIG A time output will occur when the IRIG signal lock is lost after synchronization. The IRIG lock status is indicated by a front panel LED.

2.2 IRIG A TIME OUTPUT

The IRIG A output provides IRIG time in un-modulated level shift format. Three buffered TTL, positive true outputs are provided.

2.3 IRIG B TIME OUTPUT

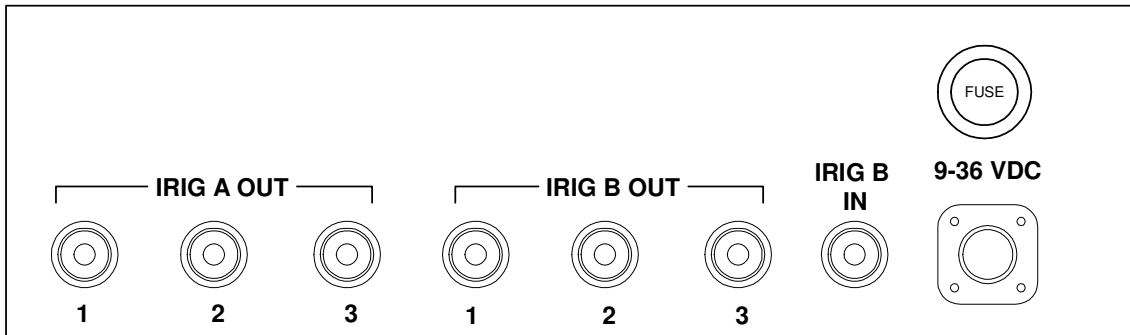
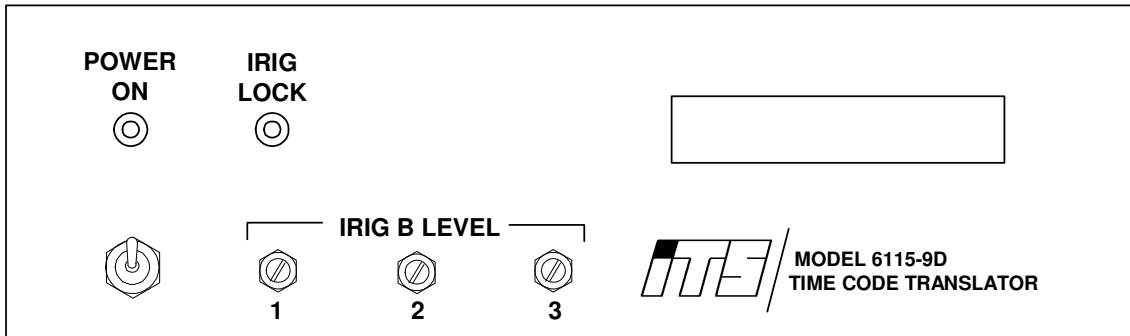
Three Buffered IRIG B outputs with individual front panel level controls are provided. These outputs are not available when the 6115-9D is operating without an IRIG B input.

3.0 MECHANICAL CONFIGURATION

The 6115-9D is housed in a 8.5 x 8.5 x 2.5 inch aluminum enclosure. (See fig 1). All circuitry is on printed wiring boards. All internal connections are via mating connectors, no edge connectors are used.

All connectors are on the rear of the enclosure and all controls and indicators are on the front panel.

Figure 1
6115-9D Enclosure



4.0 CONTROLS AND INDICATORS (See Figure 1)

- | | | |
|-----|-------------------------|--|
| 4.1 | Power | 2 Position Toggle Switch -
Selects power ON/OFF. LED illuminates when unit is ON. |
| 4.2 | IRIG B Level | Three, Screwdriver adjustable potentiometer –
Provides means to set the IRIG B output level of each of the three outputs individually. |
| 4.3 | IRIG Lock | Yellow LED Indicator -
Illuminated when unit is locked to an applied IRIG B signal. |
| 4.4 | LCD Display | 16 Character LCD Display -
Displays time, and status. |
| 4.5 | IRIG B Input | BNC Connector -
Receives IRIG B serial time reference signal. |
| 4.6 | IRIG A Output (1, 2, 3) | Three BNC Connectors –
Outputs IRIG A000 time code. |
| 4.7 | IRIG B Output (1, 2, 3) | Three BNC Connectors –
Outputs buffered IRIG B time code. |
| 4.8 | Power | KPT02A8-4P Connector -
External power (9-36VDC or 9-30 VAC input range).
Pinout as follows:
A - AC/DC Power In
B - AC/DC Power return
C - Chassis ground
D - n/u |

5.0 SPECIFICATIONS

5.1	Frequency Stability (internal clock)	1 part in 10^6 (TCXO)
5.2	Time to Lock	Lock time from application of input time code is three seconds max.
5.3	IRIG B Input	Standard IRIG B serial time code signal. IAW OSG IRIG Standard 200-98. Input level 0.5 to 15 volts peak to peak with modulation ratio from 2:1 to 6:1.
5.4	IRIG A Outputs	TTL levels, positive true, time referenced to positive going edge. IRIG A000 format. Three buffered outputs provide 350 milliamps maximum.
5.5	IRIG B Outputs	Outputs IRIG B identical to applied IRIG B signal except buffered and with level set by front panel controls. Level range is 1 to 10 volts peak to peak with 3 volts p-p in. 100 milliamps maximum.
5.6	Power	9 to 36 Volts DC or 9 to 30 Volts AC
5.7	Temperature	
	Operating	0°C to +55°C
	Non-operating	-10 to +70
5.8	Humidity	95% non-condensing
5.9	Package	
	Size	8.5" wide x 8.5" deep x 2.5" high
	Weight	2.5 lbs.